

Date Searched: 4/11/2005

Databases Searched: USPAT, USPGPUB

Plurals: ON

Terms Searched:

L1 ((716/1-18.ccls. or 326/38-45,47,63-81,101-103.ccls. or 710/10.ccls. or 713/1-100.ccls.) and (program\$5 or configur\$6 or reprogram\$5 or re-program\$5 or reconfigur\$6 or re-configure\$6 or initial\$8 or reinitial\$8 or re-initial\$8) and ((input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux or buffer\$3) same (convers\$4 or convert\$3 or shift\$3 or scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3))) and (clock same (phase adj shift\$3)) and @pd>"20041101"

Results: 5 hits

L2 (scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3) same polynomial

L3 1 and 2

Results: 0 hit

L4 716/1-18.ccls. or 326/38-45,47,63-81,101-103.ccls. or 710/10.ccls. or 713/1-100.ccls.

L5 program\$5 or configur\$6 or reprogram\$5 or re-program\$5 or reconfigur\$6 or re-configure\$6 or initial\$8 or reinitial\$8 or re-initial\$8

L6 (programmable adj2 (device or array)) or pld or pga or fpga or cpld

L7 (input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux or buffer\$3) same serial\$2 same (scrambl\$3 or encod\$3 or encrypt\$4)

L8 4 and 5 and 6 and 7 and @pd>"20041101"

Results: 5 hits

L9 6260087.uref.

Results: 9 hits

L10 5,687,325.uref.

Results: 38 hits

L11 serial same polynomial same (scrambl\$3 or encrypt\$4 or encod\$3) same bit same length

Results: 20 hits

L12 (programmable near3 (array or device)) or pld or pga or fpga or cpld

L13 embed\$3 same 12 same (non-programmable or nonprogrammable or non-configurable or nonconfigurable or fix\$3)

L14 (input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux\$3 or buffer\$3) same (convers\$4 or convert\$3 or shift\$3 or scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3)

L15 13 and 14

Results: 80 hits

L16 (event adj tim\$3) same ((phase near3 shift\$3) or delay\$3) same (clock or clk or ck)

Results: 115 hits

US 6260087 B1	USPAT Chang; Web	710/100
US 5687325 A	USPAT Chang; Web	710/104

US 6839889 B2	USPAT Liu; Ming-Kang	716/18
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US 6810434 B2	USPAT Muthujumaraswathy; Kumaraguru et al.	710/5
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US 6578104 B1	USPAT Small; Brian D. et al.	711/104
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US 6552410 B1	USPAT Eaton; David D. et al.	257/530
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US 6314551 B1	USPAT Borland; David J.	716/17
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US 6147890 A	USPAT Kawana; Keiichi et al.	365/49
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US 6094065 A	USPAT Tavana; Danesh et al.	326/39
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US 5959466 A	USPAT McGowan; John E.	326/39
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US 6803785 B1	USPAT May; Roger et al.	326/39
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US 6864710 B1	USPAT Lacey; Timothy M. et al.	326/39
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US 20050071800 A1

US-PGPUB

Liu, Ming-Kang 716/18

(serial communication w/ polynomial scrambling)

US 20010056561 A1 US-PGPUB Rub, Bernardo 714/746

US 5875200 A USPAT Glover; Neal et al. 714/784

US 4870346 A USPAT Mydill; Marc R. et al. 324/73.1

US 6724829 B1 USPAT Tzukerman; Shimon et al. 375/295

US 6628725 B1 USPAT Adam; Joel Fredric et al. 375/259

US 6539051 B1 USPAT Grivna; Edward L. 375/219

US 6426649 B1 USPAT Fu; Robert et al. 326/41

US 6052773 A USPAT DeHon; Andre et al. 712/43

US 6049487 A USPAT Plants; William C. et al. 365/189.04

US 20050069224 A1 US-PGPUB Nowicki, Tomasz J. et al. 382/298

(configuration data stored during manufacturing)

US 6148384 A USPAT Devanagundy; Uday N. et al. 711/163

(phase shift clocks for timing circuits)

US 5488646 A USPAT Jones; Anthony M. et al. 377/47

US 3851258 A USPAT Freedman; David Daniel 327/115

US 6803785 B1 USPAT May; Roger et al. 326/39

US 6791353 B1 USPAT Beal; Samuel W. et al. 326/39

US 5825202 A USPAT Tavana; Danesh et al. 326/39

US 6590419 B1 USPAT Betz; Vaughn et al. 326/47

US 6236229 B1 USPAT Or-Bach; Zvi 326/39

Date Searched: 11/1/2004

Databases Searched: USPAT, USPGPUB

Plurals: ON

Terms Searched:

L1 716/1-18.ccls. or 326/38-45,47,63-81,101-103.ccls. or 710/10.ccls. or 713/1-100.ccls.

L2 (program\$5 or configur\$6 or reprogram\$5 or re-program\$5 or reconfigur\$6 or re-configure\$6 or initial\$8 or reinitial\$8 or re-initial\$8)

L3 (input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux or buffer\$3) same (convers\$4 or convert\$3 or shift\$3 or scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3)

L4 1 and 2 and 3

Results: 6373 hits

L5 clock same (phase adj shift\$3)

L6 4 and 5

Results: 85 hits

L7 (scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3) same polynomial

L8 6 and 7

Results: 0 hits

L9 4 and 7

Results: 10 hits

L10 (program\$5 adj3 (device or array)) or pga or fpga or pld or cpld

L11 4 and 10

Results: 2608 hits

L12 (multiplex\$3 or mux) and (convers\$4 or convert\$3 or shift\$3) and (scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3) and (clock same (shift\$3 or delay or retim\$3 or re-tim\$3)) and 11

Results: 441 hits

L13 12 and 7

Results: 2 hits

L15 (programmable adj2 (device or array)) or pld or pga or fpga or cpld
L16 (input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux or buffer\$3) same serial\$2 same (scrambl\$3 or encod\$3 or encrypt\$4)

L17 16 same polynomial

L18 1 and 2 and 15 and 17

Results: 0 hit

L19 1 and 2 and 15 and 16

Results: 44 hits

US 6181164 B1	USPAT Miller, Andrew J.	326/46
US 6609167 B1	USPAT Bastiani, Vincent J. et al.	710/104
US 20030014620 A1	US-PGPUB Hanjani, Hassan	713/1
US 20010033188 A1	US-PGPUB Aung, Edward et al.	327/141
US 6763506 B1	USPAT Betz, Vaughn Timothy et al.	716/6
US 6525562 B1	USPAT Schultz, David P. et al.	326/39
US 6487648 B1	USPAT Hassoun, Joseph H.	711/167
US 6462577 B1	USPAT Lee, Andy L. et al.	326/40
US 6442739 B1	USPAT Palermo, Robert J. et al.	716/6
US 6437713 B1	USPAT Lesea, Austin H.	341/78
US 6373278 B1	USPAT Sung, Chiakang et al.	326/38
US 6292923 B1	USPAT Genrich, Thad J. et al.	714/795
US RE37048 E	USPAT McCollum, John L.	708/230
US 6107826 A	USPAT Young, Steven P. et al.	326/41
US 6060902 A	USPAT Albu, Lucian R. et al.	326/38
US 20030208723 A1	US-PGPUB Killian, Earl A. et al.	716/1
US 6525678 B1	USPAT Veenstra; Kerry S. et al.	341/55
US 6317804 B1	USPAT Levy; Paul S. et al.	710/305
US 6260087 B1	USPAT Chang; Web	710/100
US 6212639 B1	USPAT Erickson; Charles R. et al.	713/200
US 6008666 A	USPAT Conn; Robert O.	326/39
US 5892961 A	USPAT Trimberger, Stephen M.	712/10
US 5745734 A	USPAT Craft; David John et al.	716/16
US 6732068 B2	USPAT Sample; Stephen P. et al.	703/24

Databases Searched: EPO, JPO, IBM TDB, Derwent

Plurals: ON

Terms Searched:

L1 (program\$5 or configur\$6 or reprogram\$5 or re-program\$5 or reconfigur\$6 or re-configure\$6 or initial\$8 or reinitial\$8 or re-initial\$8)

L2 (input\$3 or output\$3 or communicat\$4 or transfer\$4 or "i/o" or "i/os" or (input adj output) or multiplex\$3 or mux or buffer\$3) same (convers\$4 or convert\$3 or shift\$3 or scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3)

L3 clock near10 (phase adj shift\$3)

L4 (scrambl\$3 or encrypt\$4 or decrypt\$4 or encod\$3 or decod\$3) same polynomial

L5 (program\$5 adj3 (device or array)) or pga or fpga or pld or cpld

L6 1 and 2 and 3

Results: 125 hits

L7 1 and 2 and 4

Results: 127 hits

L8 7 and 5

Results: 2 hits

L9 5 and 6

Results: 8 hits

US 6154869 A
US 6326812 B

DERWENT
DERWENT

WOLF, T D
JEFFERSON, D

Database Searched: IEE/IEEE Xplore

Terms Searched: (program* or configur* or reprogram* or re-program* or reconfigur* or re-configure* or initial* or reinitial* or re-initial*) and

(input* or output* or communicat* or transfer* or "i/o" or "i/os" or (input <near/1> output) or multiplex* or mux or buffer*) and (convers* or convert* or shift* or scrambl* or encrypt* or decrypt* or encod* or decod*) and

((program* <near/3> (device or array)) or pga or fpga or pld or cpld) and polynomial

Results: Your search matched **7** of **1085387** documents

Terms Searched: (program* or configur* or reprogram* or re-program* or reconfigur* or re-configure* or initial* or reinitial* or re-initial*) and

(input* or output* or communicat* or transfer* or "i/o" or "i/os" or (input <near/1> output) or multiplex* or mux or buffer*) and (convers* or convert* or shift* or scrambl* or encrypt* or decrypt* or encod* or decod*) and

((program* <near/3> (device or array)) or pga or fpga or pld or cpld)

Results: Your search matched **497** of **1085387** documents

Terms Searched: (program* or configur* or reprogram* or re-program* or reconfigur* or re-configure* or initial* or reinitial* or re-initial*) and

(input* or output* or communicat* or transfer* or "i/o" or "i/os" or (input <near/1> output) or multiplex* or mux or buffer*) and (convers* or convert* or shift* or scrambl* or encrypt* or decrypt* or encod* or decod*) and

((program* <near/3> (device or array)) or pga or fpga or pld or cpld)

and (multiplex* or mux) and (convers* or convert* or shift*) and (scrambl* or encrypt* or decrypt* or encod* or decod*) and (clock and (shift* or delay or retim* or re-tim*))

Results: Your search matched **0** of **1085387** documents

Terms Searched: (program* or configur* or reprogram* or re-program* or reconfigur* or re-configure* or initial* or reinitial* or re-initial*) and (input* or output* or communicat* or transfer* or "i/o" or "i/os" or

(input <near/1> output) or multiplex* or mux or buffer*) and (convers* or convert* or shift* or scrambl* or encrypt* or decrypt* or encod* or decod*) and ((program* <near/3> (device or array)) or pga or fpga or pld or cpld) and (multiplex* or mux) and (convers* or convert* or shift*) and (scrambl* or encrypt* or decrypt* or encod* or decod*) and clock

Results: Your search matched **1** of **1085387** documents